

Notice of References Cited	Application/Control No. 10/650,601		Applicant(s)/Patent Under Reexamination LIN, SHI-CHI	
	Examiner José R. Díaz		Art Unit 2815	Page 1 of 1

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	U	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1-Process Technology, 1986, Lattice Press, Vol. 1, pages 155-158
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.